

Vortex86SX Windows CE Development Note

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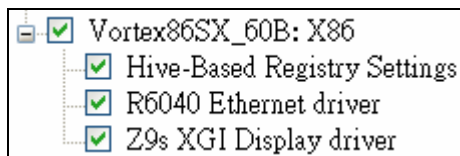
After installing Vortex86SX BSP for Windows Embedded CE 6.0, developer can start Windows CE development for Vortex86SX boards. Here are notes for your reference:

R6040 Ethernet

R6040 is the built-in 10/100Mb Ethernet in Vortex86SX SoC. In order to use KITL to debug, please use eboot.bin at "Vortex86SX_60A\Src\Bootloader\eBoot\bin".

XGI Z9s Display Driver

Z9s graphic chipset is for most Vortex86SX boards with video function. Vortex86SX BSP will not add XGI display driver by default. The default display driver is VGA flat driver in Platform Builder. If your evaluation boards have video function, please add Z9s VGA manually.



Developer can change display resolution by modifying registry settings. Search "BSP_DISPLAY_XGI" in platform.reg. You can find those registry settings:

```
[HKEY_LOCAL_MACHINE\Drivers\Display]
"ModeNumber"=dword:3
```

Here is detail modes list:

640x480x16@60Hz	0	1024x768x16@60Hz	6	640x480x32@60Hz	12	1024x768x32@60Hz	18
640x480x16@75Hz	1	1024x768x16@75Hz	7	640x480x32@75Hz	13	1024x768x32@75Hz	19
640x480x16@85Hz	2	1024x768x16@85Hz	8	640x480x32@85Hz	14	1024x768x32@85Hz	20
800x600x16@60Hz	3	1280x1024x16@60Hz	9	800x600x32@60Hz	15	1280x1024x32@60Hz	21
800x600x16@75Hz	4	1280x1024x16@75Hz	10	800x600x32@75Hz	16		
800x600x16@85Hz	5	1280x1024x16@85Hz	11	800x600x32@85Hz	17		

For LVDS, display driver will read settings from BIOS and **ModeNumber** is used as color depth: 0 for 8BPP, 1 for 16BPP, 2 for 24BPP and 3 for 32 BPP.

Using Hive-Based Registry

Enable component "Hive-Based Registry Settings" in BSP and all settings for hive-based registry will be added into your workspace. It will help developer to use hive-based registry easily.

Using 128 MB RAM

The BSP only support 64MB RAM by default. If there are 128MB RAM on your Vortex86SX board, add environment variable (Platform -> Settings -> Environment -> New) "**IMGRAM128=1**".

Serial Ports

User can find the I/O address of COM1 in **platform.reg** is 0x2F8 and IRQ is 3. This is because Windows CE uses the first serial port as debug port. Here is the table of serial ports setting in Windows CE:

Windows CE	I/O Address	IRQ	Normal PC
Debug Serial	0x3F8		COM1:
COM1:	0x2F8	3	COM2:
COM2:	0x3E8	4	COM3:
COM3:	0x2E8	5	COM4:

If user opens "COM1:" to send data in Windows CE, data will be sent to serial port 2. Follow those steps to disable debug serial port in Windows CE:

1. Open "%_WIN_DIR%\PLATFORM\COMMON\SRC\X86\COMMON\OTHER\debug.c" in Platform Builder 6.0:

```
void OEMInitDebugSerial(void)
{
    // Locate bootargs (this is the first opportunity the OAL has to initialize this global).
    //
    InitBootInfo ((BOOT_ARGS *) ((ULONG)(*(PBYTE *)BOOT_ARG_PTR_LOCATION) | 0x80000000));

    g_pX86Info->ucComPort = 0; //-- Add this line
    switch ( g_pX86Info->ucComPort ) {
    case 1:
        IoPortBase = (PUCHAR)COM1_BASE;
        break;

    case 2:
        IoPortBase = (PUCHAR)COM2_BASE;
        break;

    case 3:
        IoPortBase = (PUCHAR)COM3_BASE;
        break;

    case 4:
        IoPortBase = (PUCHAR)COM4_BASE;
        break;

    default:
        IoPortBase = 0;
        break;
    }
}
```

2. Platform Builder -> Build -> Open Build Release Directory.
3. Change work directory to "%_WIN_DIR%\PLATFORM\COMMON\SRC\X86\COMMON\OTHER "
4. Run "build".
5. The registry settings for serial ports are okay in platform.reg in Vortex86SX BSP. It is no need to modify.
6. If you need COM4, add this environment variable "**BSP_SERIAL4=1**".
7. Build your image.

Technical Support

For more technical support, please visit <http://www.dmp.com.tw/tech/vortex86sx> or mail to tech@dmp.com.tw.